

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	(memory and gate and substrate and opening and through\$1hole and thickness and capacitor).clm.	USPAT	OR	OFF	2005/08/31 14:39
L2	0	(memory and gate and substrate and opening and through\$1hole and thickness and capacitor).clm.	US-PGPUB	OR	OFF	2005/08/31 14:39

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